

FIGURE 1 -- PROGRAMMABLE PULSE SEQUENCER

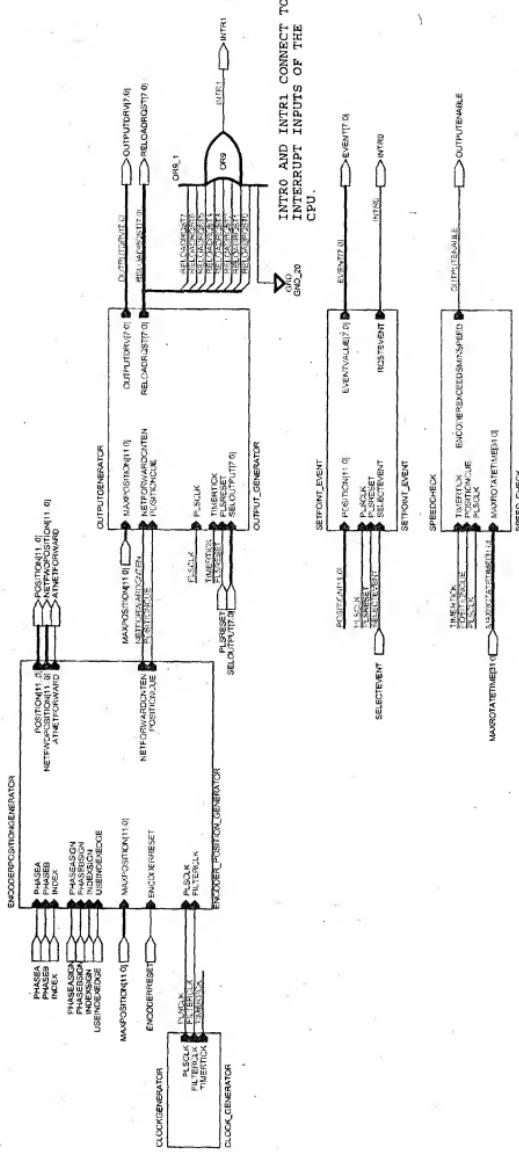


FIGURE 2 -- CLOCK GENERATOR

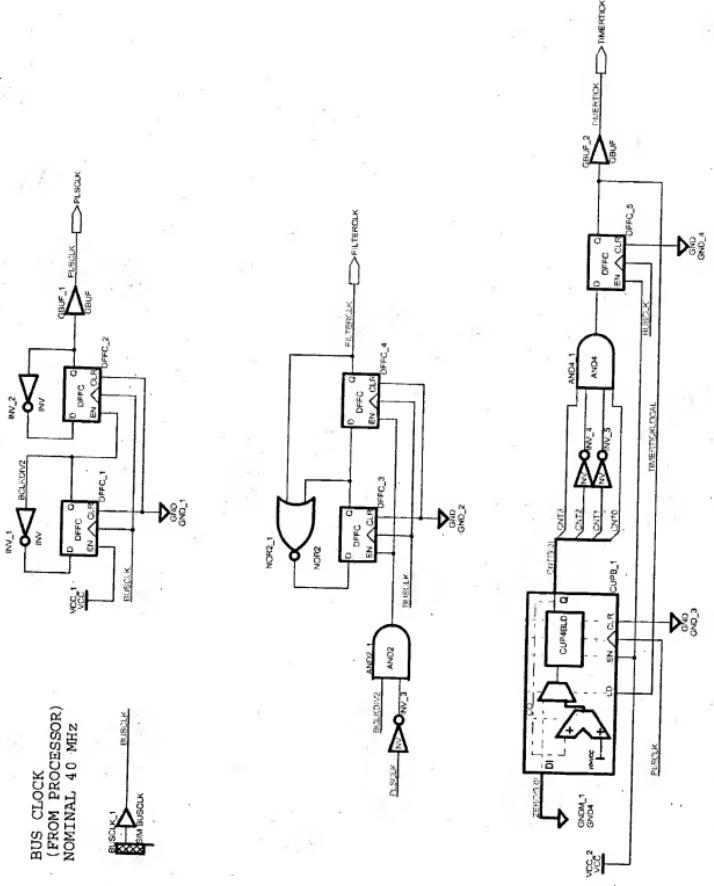


FIGURE 3 -- ENCODER POSITION GENERATOR

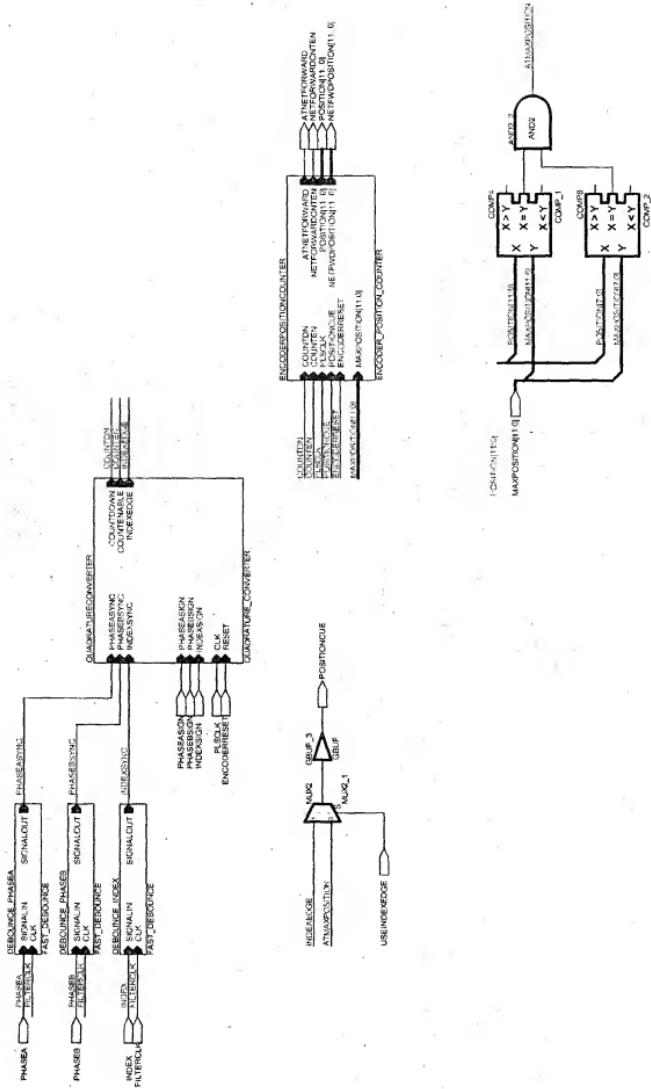


FIGURE 4 -- 'FAST SIGNAL DEBOUCE

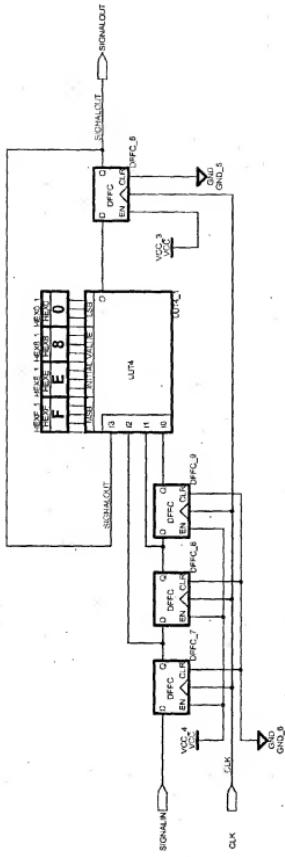


FIGURE 5 -- QUADRATURE CONVERTER

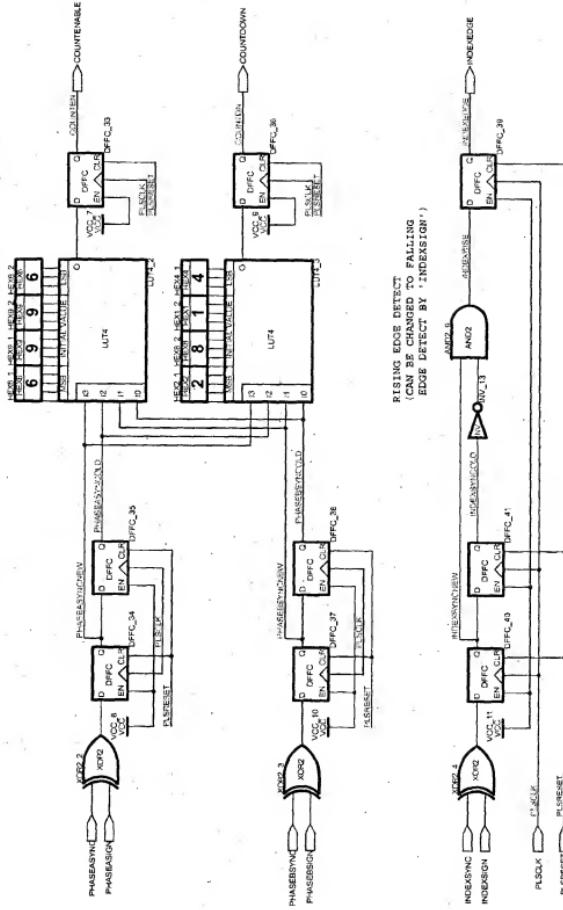


FIGURE 6 -- ENCODER POSITION COUNTER WITH NET FORWARD POSITION REGISTER

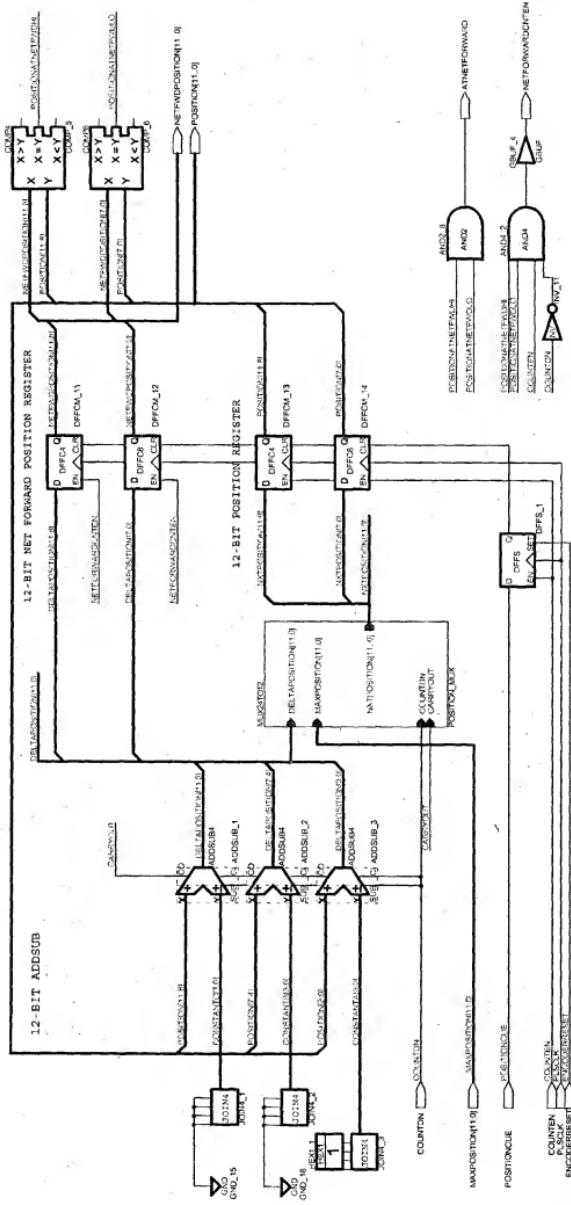


FIGURE 7 -- POSITION MULTIPLEXOR

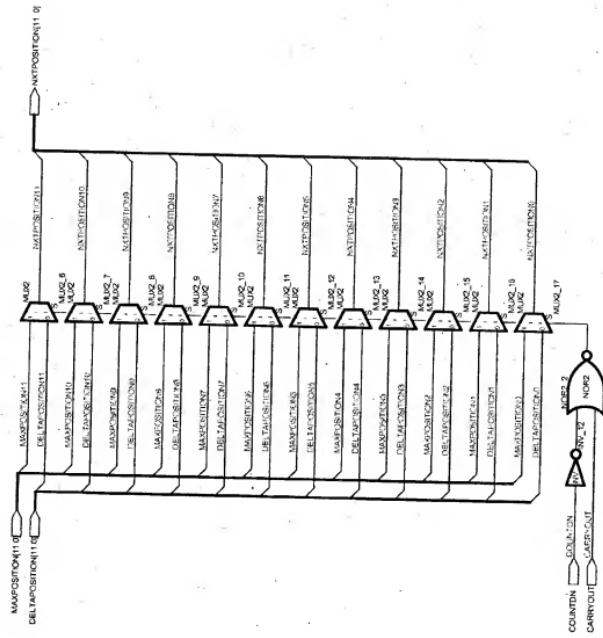


FIGURE 8 -- OUTPUT GENERATOR

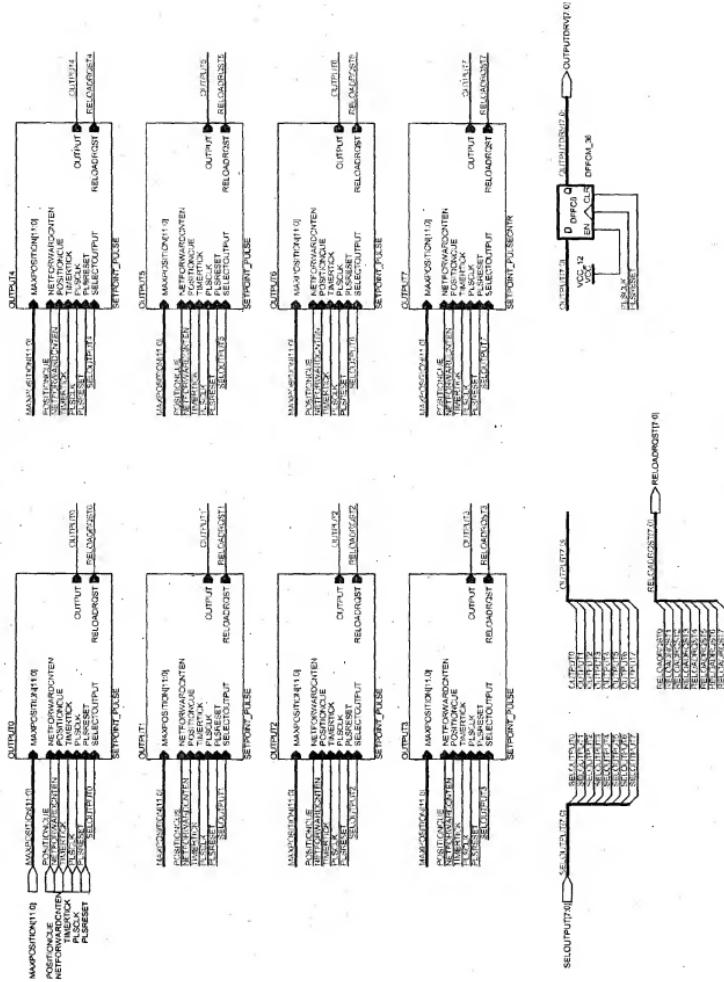


FIGURE 9 -- SETPOINT PULSE MODULE

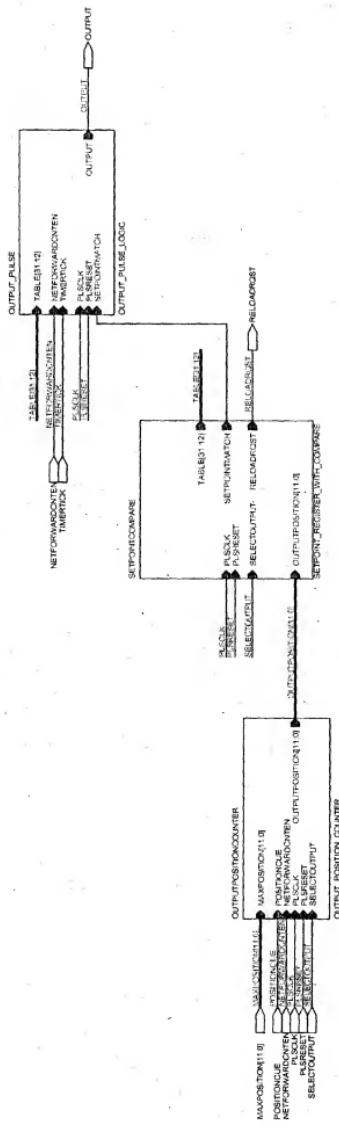


FIGURE 10 -- OUTPUT POSITION COUNTER

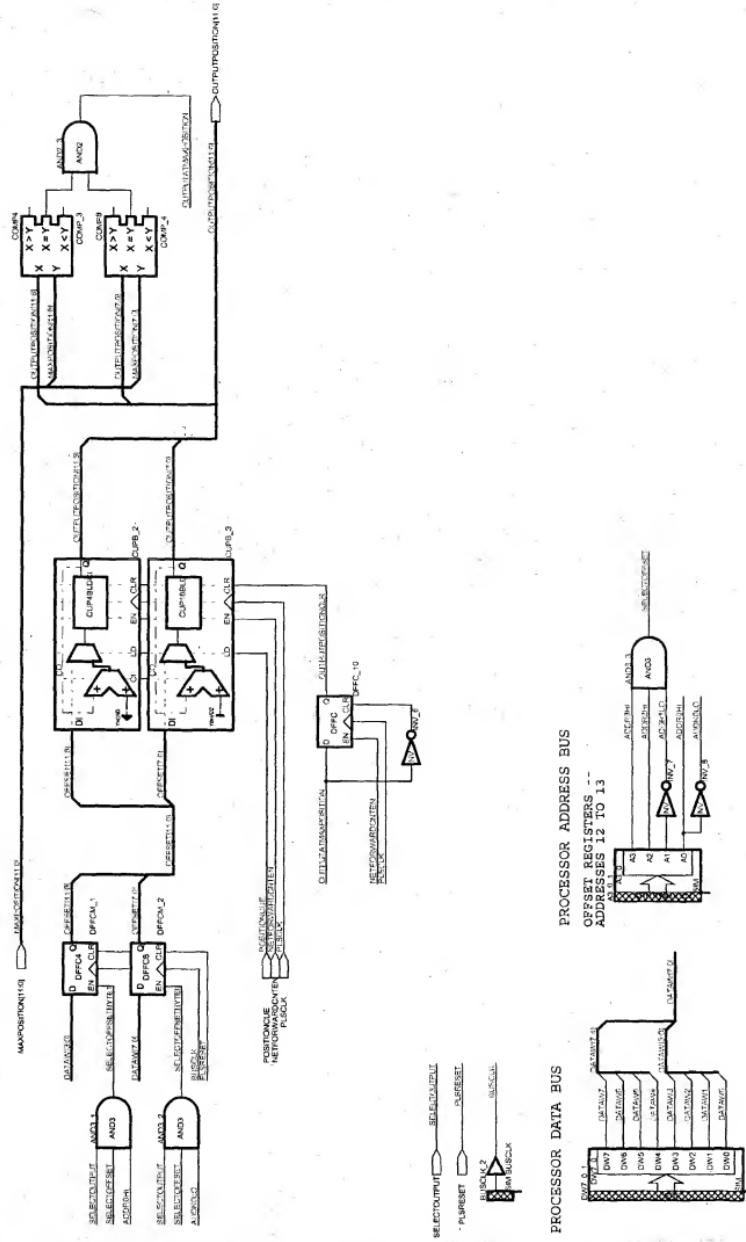


TABLE REGISTERS

FIGURE 11 -- SETPOINT REGISTER WITH COMPARE

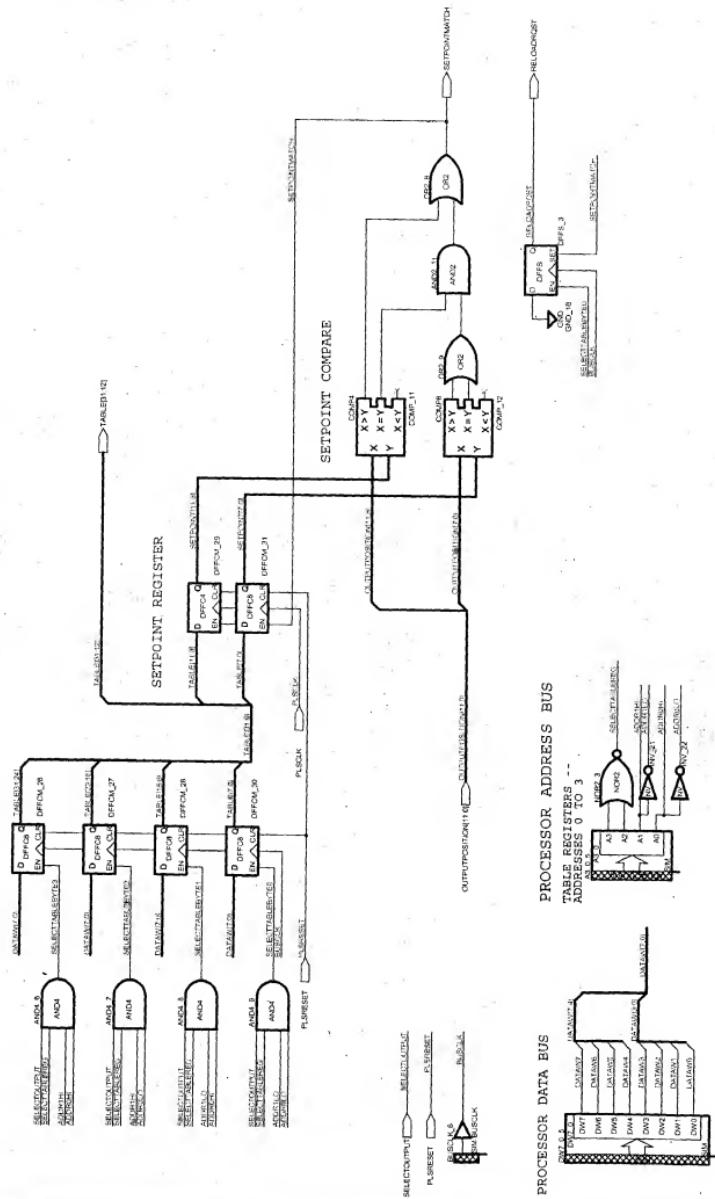


FIGURE 12 -- OUTPUT PULSE LOGIC

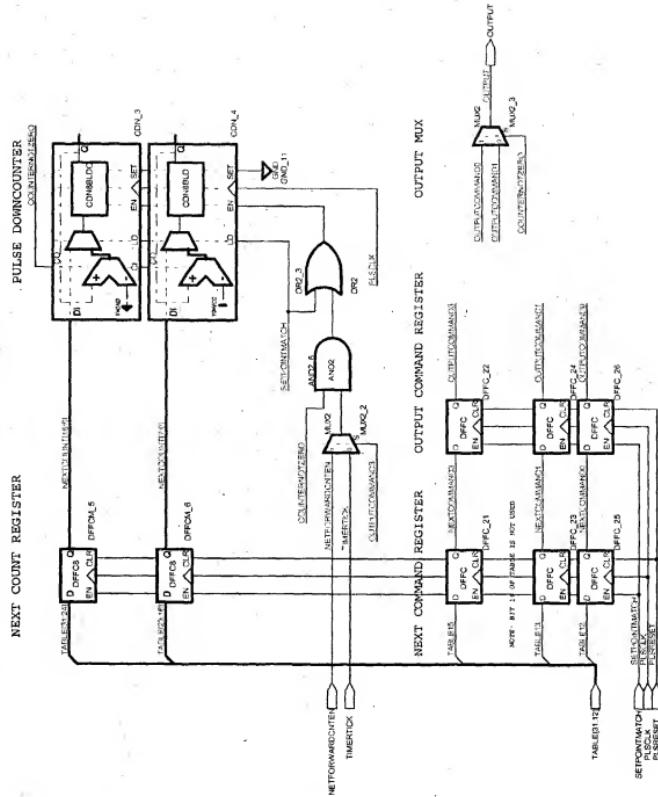


FIGURE 13 -- OUTPUT PULSE LOGIC WITH ADJUSTABLE WIDTH

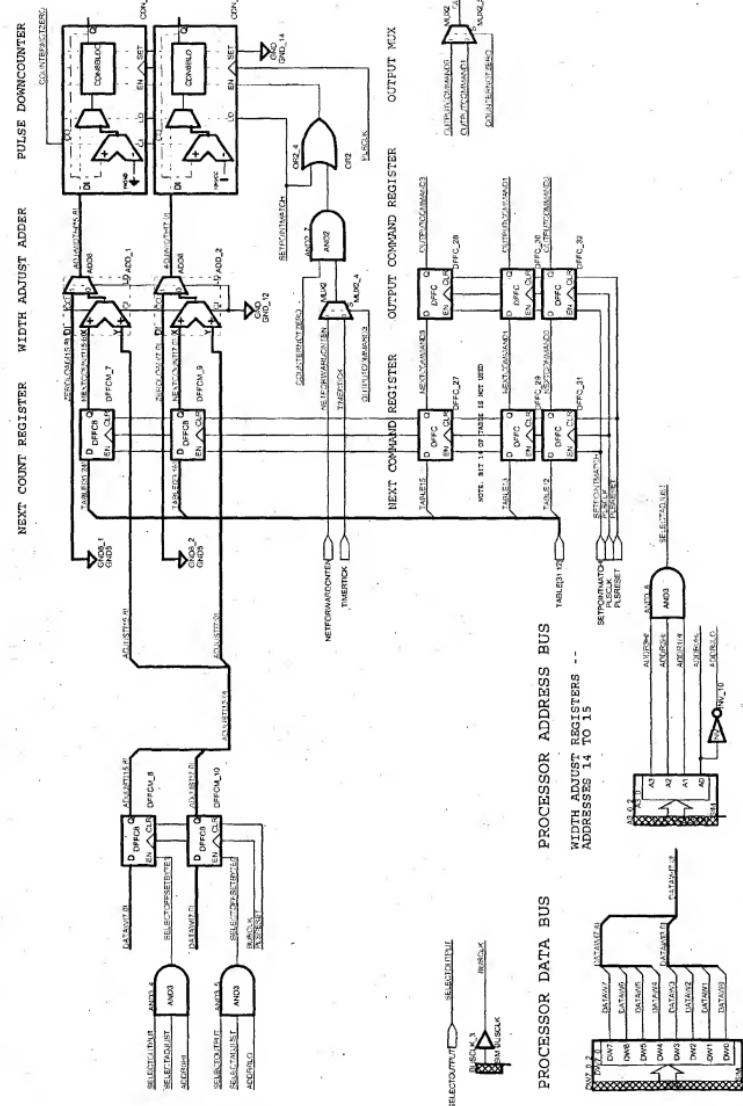


FIGURE 14 -- SETPOINT PULSE MODULE WITH OUTPUT PULSE COUNTER

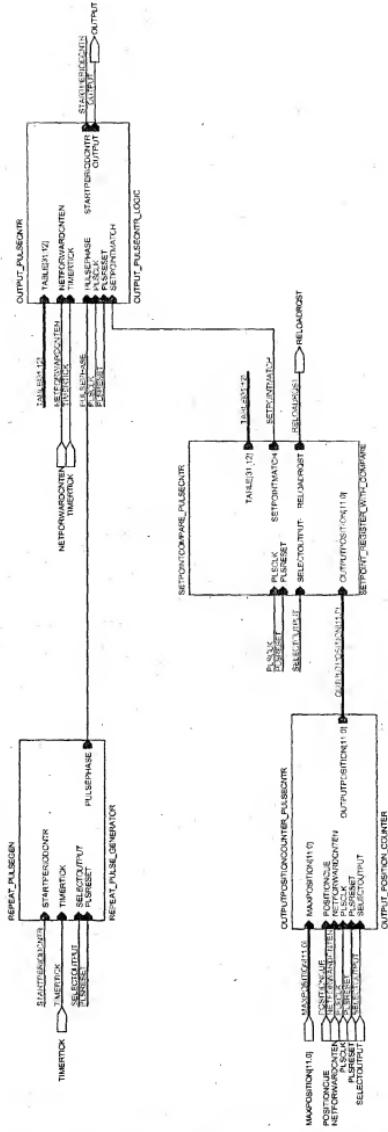


FIGURE 15 -- REPEAT PULSE GENERATOR

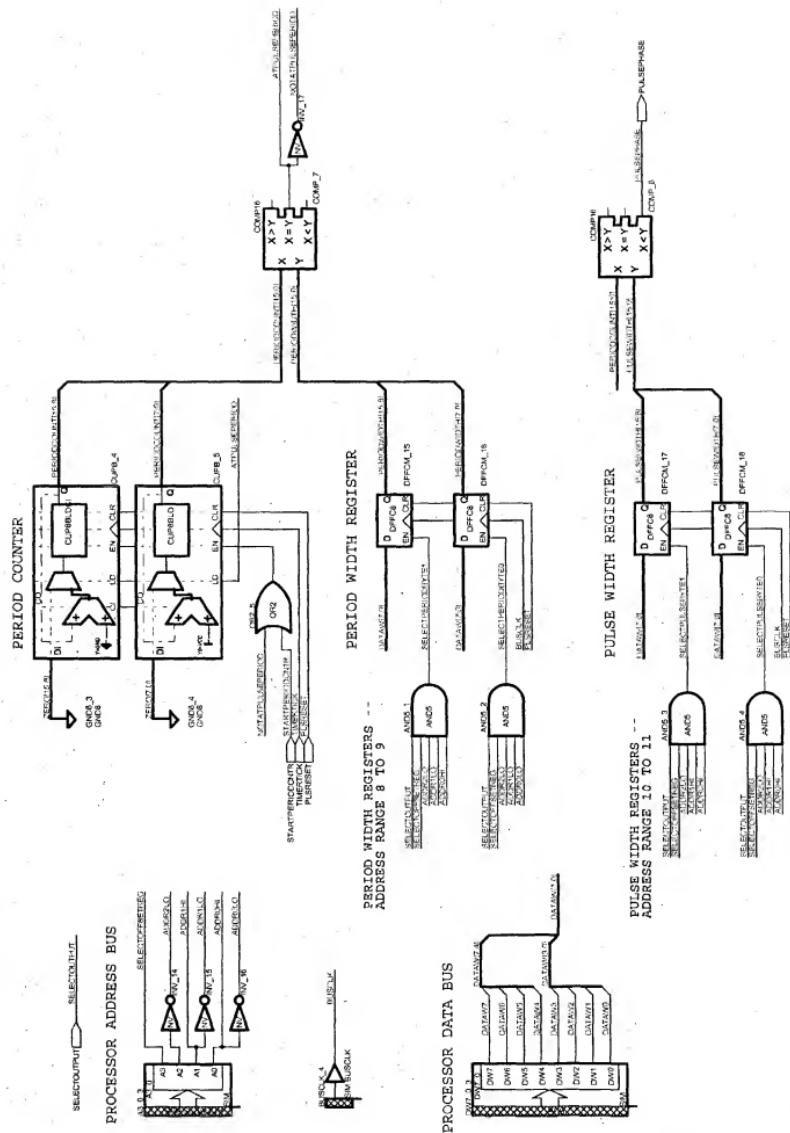


FIGURE 16: -- OUTPUT PULSE CNTR LOGIC

BEST DOCUMENTATION

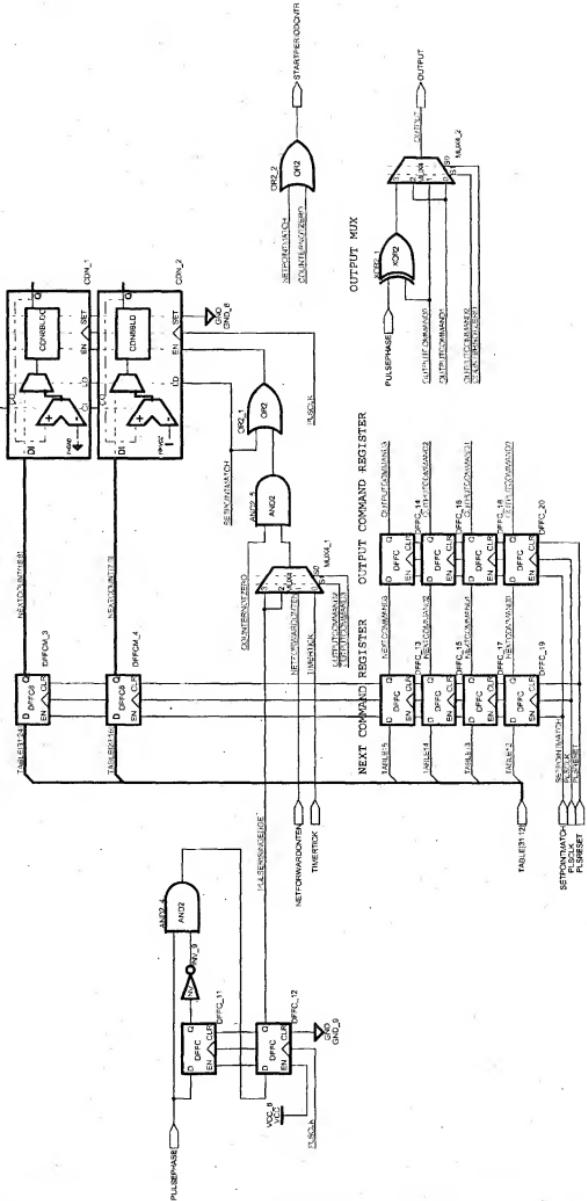


FIGURE 17 -- SET POINT EVENT

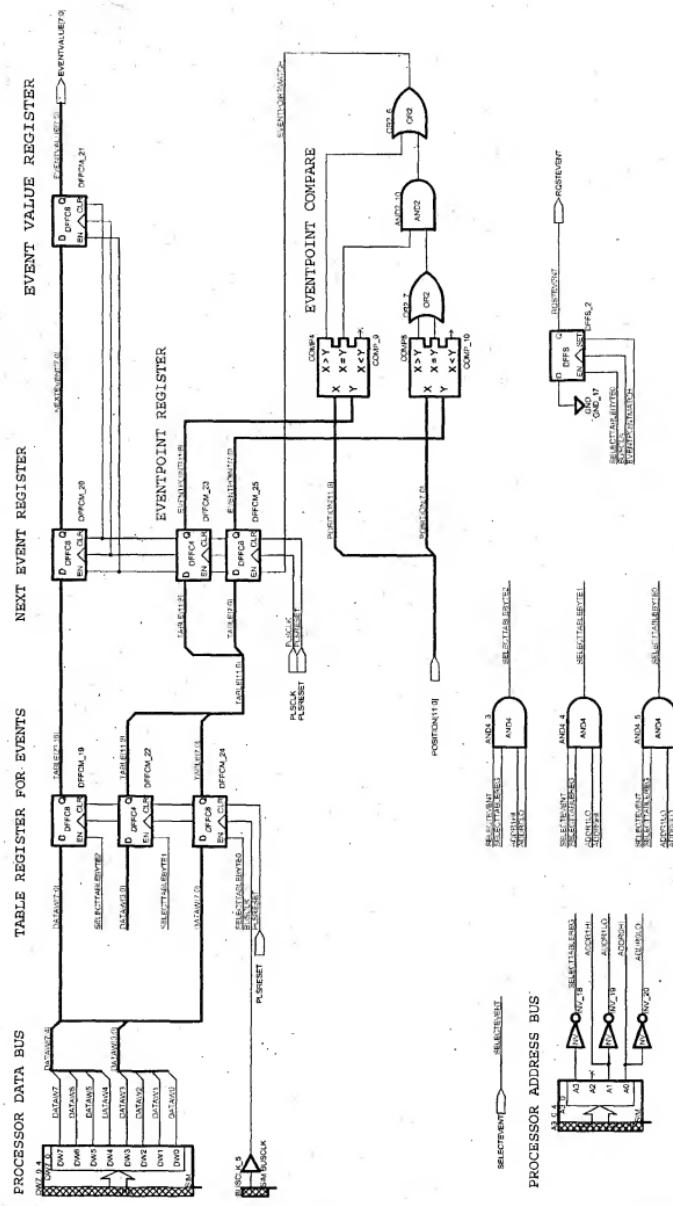


FIGURE 18 -- SPEED CHECK MODULE
 ROTATION COUNTER
 ROTATION TIME
 REGISTER

